

PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE CITATION

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Sheet 1 of 1

SERIAL NO.
10/773,050
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U.S. PATENT DOCUMENTS						
Ref. Desig.	Examiner's Initials	Document Number	Date	Name	Class/ Subclass	(If appropriate) Filing Date
1.	PC	5,504,780	4/2/1996	Alspector et al.		

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1.	PC	0 385 873	01.03.90	EP			

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1.	PC		Waheed, K. et al, "A Mixed-Mode Design for a Self-Programming Chip for Real-Time Estimation, Prediction, and Control", IEEE August 8, 2000, pp. 798-801, Vol. 2, XP002267579.
2.			Waheed, K. et al., "A Mixed Mode Self-Programming Neural System-on-Chip for Real-Time Applications", IEEE July 15, 2001, pp. 195-200, Vol. 1, XP002267580.
3.	V	/	European Patent Office, International Search Report, February 13, 2004.

Examiner: /Peter Coughlan/ Date Considered: 08/15/2006